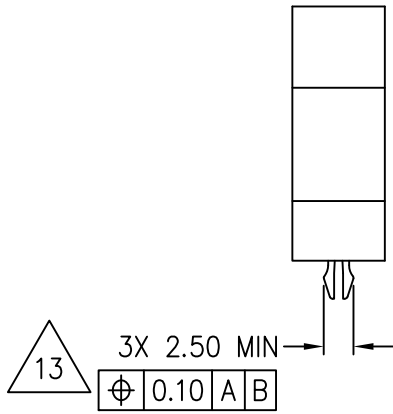


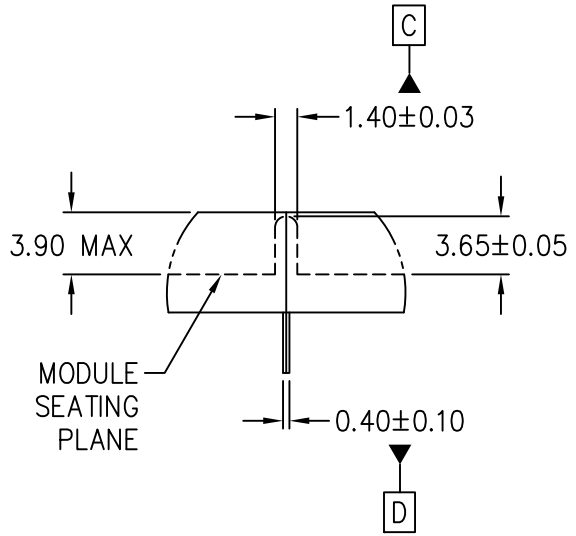
JEDEC SOLID STATE  
 PRODUCT OUTLINE  
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THIS *REGISTERED OUTLINE* HAS BEEN PREPARED BY THE JEDEC JC-11 COMMITTEE AND REFLECTS A PRODUCT WITH ANTICIPATED USAGE IN THE ELECTRONICS INDUSTRY; CHANGES ARE LIKELY TO OCCUR.

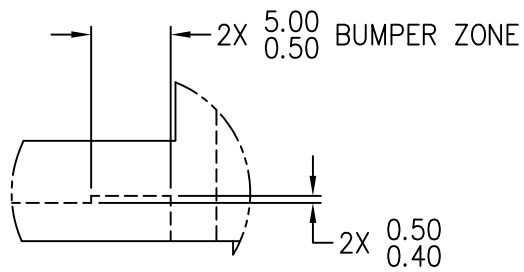
TITLE	PACKAGE DESIGNATOR	NUMBER	ISSUE	DATE	SHEET
DDR4 DIMM PTH 288 PIN SOCKET OUTLINE 0.85 MM PITCH	SKT	SO-016	B	SEP 2013	1 OF 9



VIEW A-A



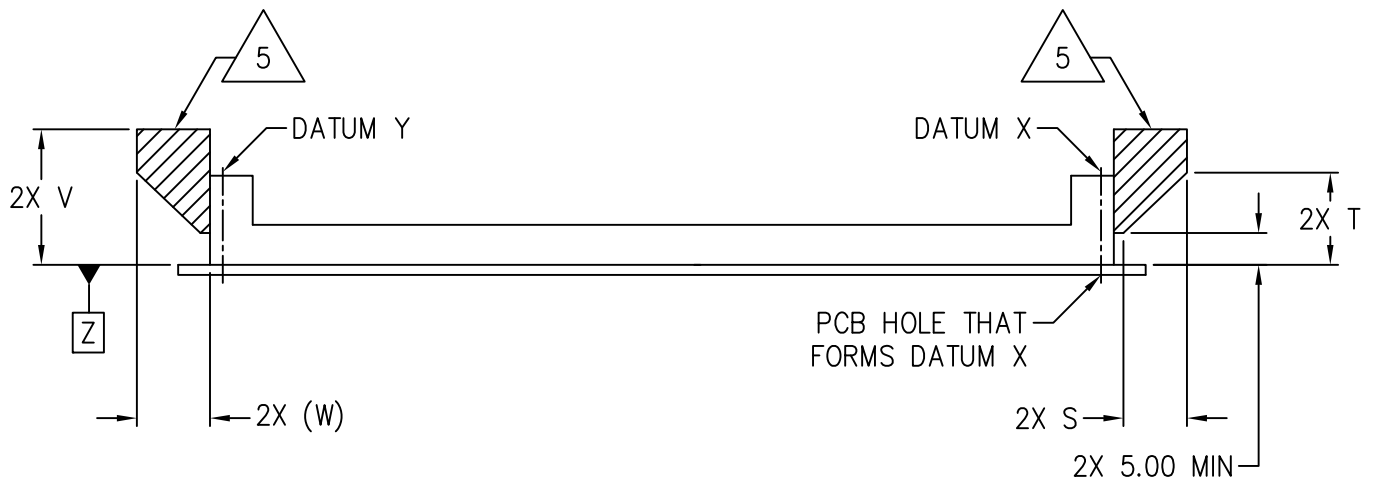
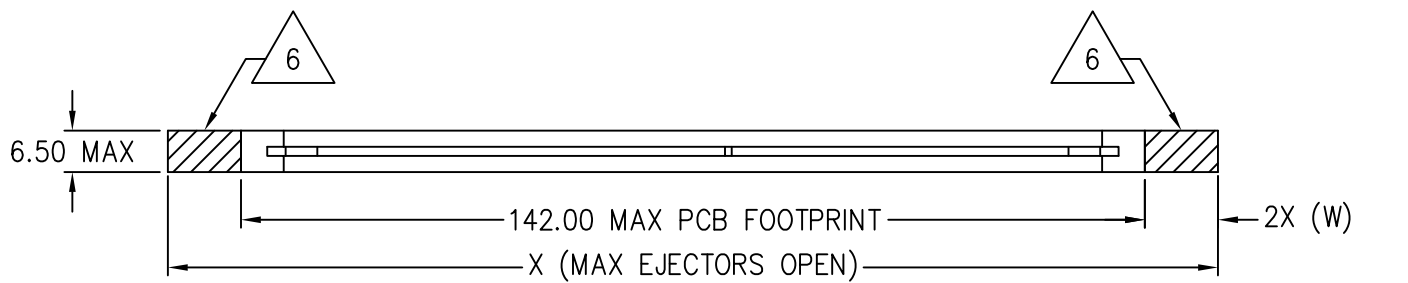
DETAIL B



DETAIL C



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SIDE VIEW (PCB AND SOCKET)

TABLE 1

VARIATIONS				
SYMBOL	MIN	NOM	MAX	NOTES
S	10.00	---	---	
T	14.50	---	---	
V	---	---	21.30	
W	10.00 REF			
X	---	---	162.00	
Y	11.60	---	---	4
Z	---	---	2.40	3
NOTES	1, 2			
REF	14-142			
ISSUE	A			

NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2009.

2. ALL DIMENSIONS ARE IN MILLIMETERS.



DIMENSION IS FROM THE BOTTOM OF THE SOCKET PCB REFERENCE, DATUM A, TO THE SEATING PLANE OF THE DDR4 DIMM IN THE SOCKET.



A MINIMUM HEIGHT OF Y MEASURED TO THE HIGHEST POINT OF THE SOCKET TO MODULE ENGAGEMENT IS REQUIRED TO PREVENT THE MEMORY MODULE TILTING RELATIVE TO DATUM B OF THE SOCKET.



KEEP OUT CROSS HATCH ARE RESERVED FOR SOCKET EJECTORS AT BOTH ENDS.



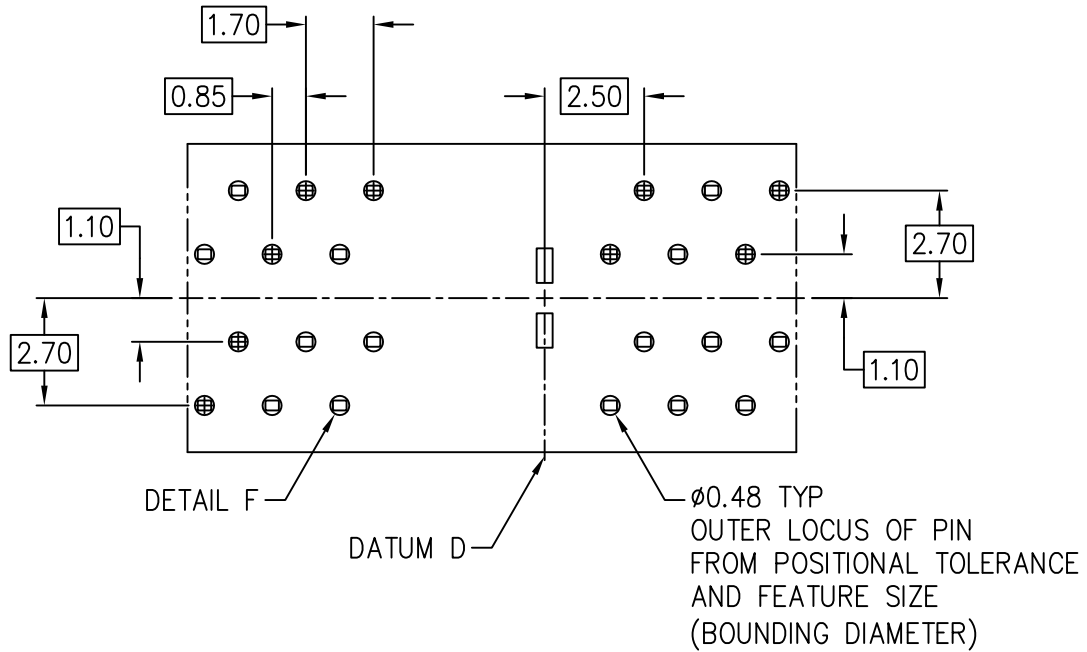
KEEP OUT ZONE IS HEIGHT LIMITED PER NOTE 5.

7. REFER TO DDR4 DIMM REGISTERED OUTLINE MO-309 FOR MODULE DIMENSIONS.

8. THE MAXIMUM INSERTION FORCE FOR THE SOCKET TO BE 126.40 N MAXIMUM. THE INSERTION FORCE SHOULD BE MEASURED USING THE JEDEC GAUGE GS-010. REFER TO EIA 364-13 FOR THE TEST PROCEDURE.

NOTES CONTINUED:

- THE CROSS SECTION OF THE SOCKET SOLDER PIN TAILS PLUS POSITION AND SIZE TOLERANCE FROM THE OUTER LOCUS BOUNDARY DIAMETER. THE FIGURE BELOW SHOWS THE OUTER LOCUS OF THE CONNECTOR SOLDER PIN TAILS.



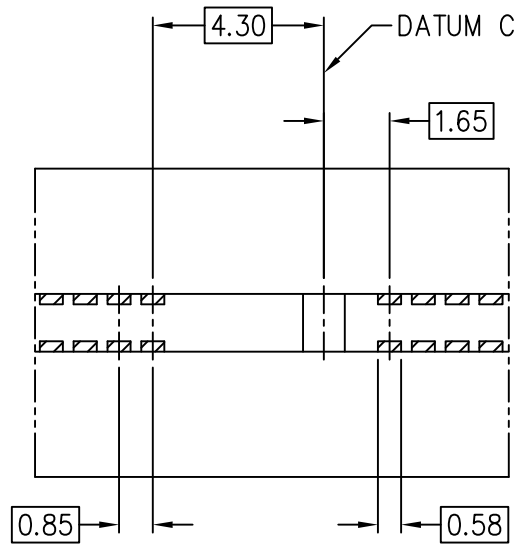
DETAIL F

OUTER LOCUS OF CONNECTOR SOLDER PIN TAILS

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NOTES CONTINUED:

- THE FIGURE BELOW SHOWS THE OUTER LOCUS OF THE CONNECTOR PINS AT DIMM MATING INTERFACE WITH RESPECT TO THE CONNECTOR KEY, DATUM C. THE WIDTH OF THE OUTER LOCUS IS DEFINED BY THE MAXIMUM PIN WIDTH PLUS THE POSITIONAL TOLERANCE OF THE PINS WITH RESPECT TO DATUM C.



OUTER LOCUS OF CONTACT PIN FROM POSITIONAL TOLERANCE AND FEATURE SIZE (BOUNDING WIDTH)

### OUTER LOCUS OF CONNECTOR CONTACT PIN

11

VARIOUS COMPANIES HAVE ISSUED PATENTS AND RELATED PATENT APPLICATIONS THAT MAY APPLY TO THIS REGISTRATION. IF THE CURRENT ISSUE PATENTS OR LATER PATENTS RESULTING FROM RELATED APPLICATION DO APPLY, THESE COMPANIES INTEND TO COMPLY WITH THE JEDEC PATENT POLICY AND LICENSE UNDER REASONABLE TERMS AND CONDITIONS THAT ARE DEMONSTRABLY FREE OF ANY UNFAIR DISCRIMINATION. REFERENCED PATENTS ARE AS FOLLOWS.

LOTES	CHINA PATENT APPLICATION NO.: CN 202759077 U
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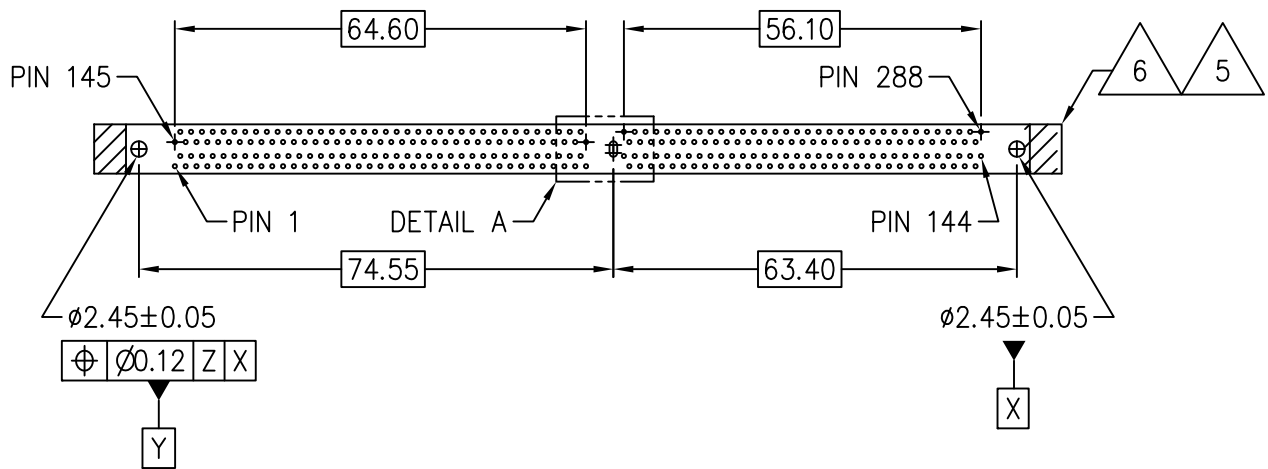
APPLICATION NOTES:

12. IN ORDER TO PREVENT BINDING, DAMAGE, OR POSSIBLE SHORTING BETWEEN CONTACTS, A VERTICAL ALIGNMENT AND INSERTION OF THE MEMORY MODULE INTO THE SOCKET IS RECOMMENDED.

13. WHEN THE SOCKET IS INSERTED INTO THE PCB, THE BOARD LOCKS ARE INTENDED TO PROVIDE A SNUG FIT TO MAINTAIN THE SOCKET FLUSH TO THE PCB AND ALLOW NO SIDE MOVEMENT.

14. REFERENCE PCB FOOTPRINT (FOR THROUGH HOLE PINS ONLY).

15. RECOMMENDED DIMENSIONS OF THESE FEATURES WERE DERIVED TO ALLOW A ROUTING SOLUTION ON THE MOTHERBOARD.

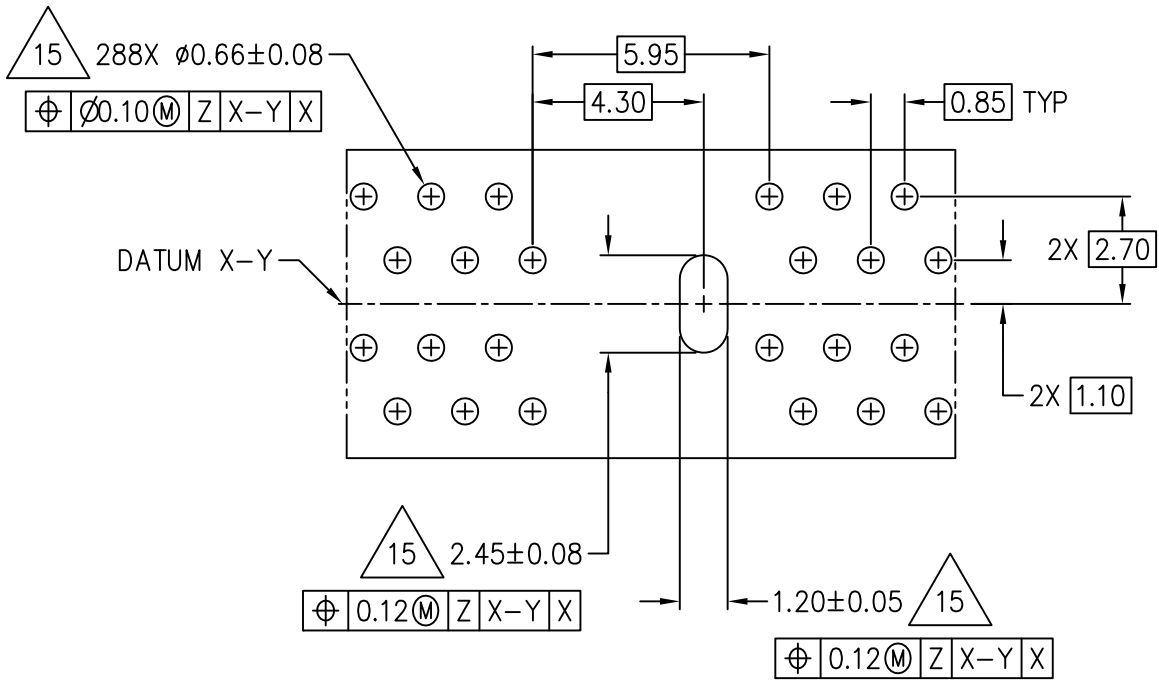


TOP VIEW (PCB FOOTPRINT ONLY)

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APPLICATION NOTES CONTINUED:



DETAIL A

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CHANGE RECORD

IF THE CHANGE INVOLVES ANY WORDS ADDED OR DELETED (EXCLUDING DELETION OF ACCIDENTALLY REPEATED WORDS), THE CHANGE IS TO BE INCLUDED BELOW. PUNCTUATION CHANGES MAY OR MAY NOT BE INCLUDED.

INITIAL ISSUE: A	DATE: SEPTEMBER 2012	ITEM NUMBER: 14-142
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CHANGE RECORD HISTORY:
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ISSUE: A.01	DATE: OCTOBER 2012	ITEM NUMBER: 14-142(E)
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LOCATION	CHANGED FROM:	CHANGED TO:
SHEET 4, NOTE 8	GS-XXX	GS-010

ISSUE: B	DATE: SEPTEMBER 2013	ITEM NUMBER: 14-155
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LOCATION	CHANGED FROM:	CHANGED TO:
ALL SHEETS	TITLE WAS ... 284 PIN ...	TITLE IS ... 288 PIN ...
SHEET 1	PIN COUNT WAS 284	ADDED BUMPER & DIM ADDED DETAIL C PIN COUNT INCREASED TO 288
SHEET 2		ADDED DETAIL C
SHEET 3		NEW SHT, MOVED FROM SHT 2
SHEET 6	FIGURE D ...	THE FIGURE BELOW ...
SHEET 7		ADDED NOTE 11
SHEET 8	63.75 DIM & 55.25 DIM PIN 142, PIN 143, PIN 284  PIN COUNT WAS 284	64.60 DIM & 56.10 DIM PIN 144, PIN 145, PIN 288 RESPECTIVELY PIN COUNT INCREASED TO 288
SHEET 9	284X $\phi 0.66 \pm 0.08$	288X $\phi 0.66 \pm 0.08$

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