

**Keithley 2001 repair - Bug #867****Fix error 410.1, 411.1, 411.2 - TRMS / TRMS Filter**

12/15/2013 03:45 AM - tin

<b>Status:</b>	Closed	<b>Start date:</b>	12/15/2013
<b>Priority:</b>	Normal	<b>Due date:</b>	12/29/2013
<b>Assignee:</b>	tin	<b>% Done:</b>	0%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	Keithley 2001 #6		

**Description**

VRIN is buffered by U517 and tied to BUFF. The signal at BUFF is routed through R233, K503, K500, the input protection circuit and Q513 to the input buffer (Q512 and U520). The buffered signal is tied to AMP IN.

The signal at AMP IN is then routed to the variable gain amplifier (VGA) and, being positive, follows the non-inverting x1 path that consists of R530, Q509, Q507, U519, R531, U509, Q501, and U516 to pin 15 of the TRMS converter U517.

The output of the U517 is routed to the multiplexer U511 through R219. The output (OUT) of the multiplexer is routed through buffer U342 to ACV/A. The signal at ACV/A is routed through U320 (/AC pulled low) and applied to the A/D buffer (U322), which is configured for x1 gain. Measure the output at A/D IN.

**History****#1 - 07/13/2014 01:38 AM - tin***- Target version changed from Keithley 2001 #7 to Keithley 2001 #6***#2 - 06/07/2016 01:11 AM - tin***- Status changed from New to Closed*