

Keithley 2001 repair - Bug #863

Fix errors 405.2, 405.4, 405.6, 405.8, - Absolute value x10 gain comparison

12/15/2013 03:40 AM - tin

Status:	Closed	Start date:	12/15/2013
Priority:	Normal	Due date:	12/22/2013
Assignee:	tin	% Done:	100%
Category:		Estimated time:	0.00 hour
Target version:			

Description

DAC U531 is programmed to generate 0.51 VDC at PRECOMP. That signal is then applied to ACF through R560 and U532 (DAC line pulled low). ACF is routed to AMP IN via U526, Q516, and the AC input buffer.

AMP IN is tied to the inverting and non-inverting paths of the variable gain amplifier (VGA). NETOUT (output of U519) is routed to the Zero-Crossing Amplifier which, based on the polarity, generates the appropriate COMP- signal that is applied to comparator U507. The comparator selects the path that the AMP IN signal will follow through the VGA by closing the appropriate analog switches of U509.

The positive (non-inverting) AMP IN signal path with the VGA at x10 is through R530, Q508, Q507, U519, R531, U509, Q501, and U516 to pin 12 of multiplexer U511. The output (OUT) of the multiplexer is routed through buffer U342 to ACV/A. The signal at ACV/A is routed through U320 (/AC pulled low) and applied to the A/D buffer (U322), which is configured for x1 gain.

Measure the voltage at A/D IN. It should be same value that was measured in test 405.1.

History

#1 - 06/07/2016 01:20 AM - tin

- Status changed from New to Closed

- % Done changed from 0 to 100