

REF-R - Bug #1022

VREF Board A01 errata

03/27/2014 04:11 PM - tin

Status: In Progress	Start date: 03/27/2014
Priority: Normal	Due date: 04/05/2014
Assignee: tin	% Done: 80%
Category:	Estimated time: 0.00 hour
Target version:	

Description

- **Supply on U5 is reversed! Schematics and layout update required**
- New placement for U3 and C2 required
- Bottom solder mask opening on voltage shape, need adjust mask layer
- Routing update for U5
- Top mask fix for LTZ pin 5
- Add copper guard on bottom etch layer with single via thru all layers for EMI

Associated revisions

Revision 7:272634419779 - 06/22/2014 01:49 AM - tin

Added fixed schematics for VREF B1 ID #1022 @4h

History

#1 - 03/28/2014 10:33 PM - tin

- File ir_top.png added

- File ir1.png added

- File ir2.png added

- File ir3.png added

- File ir4.png added

#2 - 06/22/2014 01:32 AM - tin

- % Done changed from 0 to 80

Revision B1

- Fixed U5 power
- Change 70K to VHPZ
- Added back R13
- Added C8,C9
- Adjust value C4

#3 - 03/09/2015 11:27 PM - tin

- Project changed from KX to REF-R

- Category deleted (101)

- Target version deleted (prototype)

Files

ir_top.png	47 KB	03/28/2014	tin
ir1.png	39.8 KB	03/28/2014	tin
ir2.png	28.1 KB	03/28/2014	tin
ir3.png	77.4 KB	03/28/2014	tin
ir4.png	65.2 KB	03/28/2014	tin