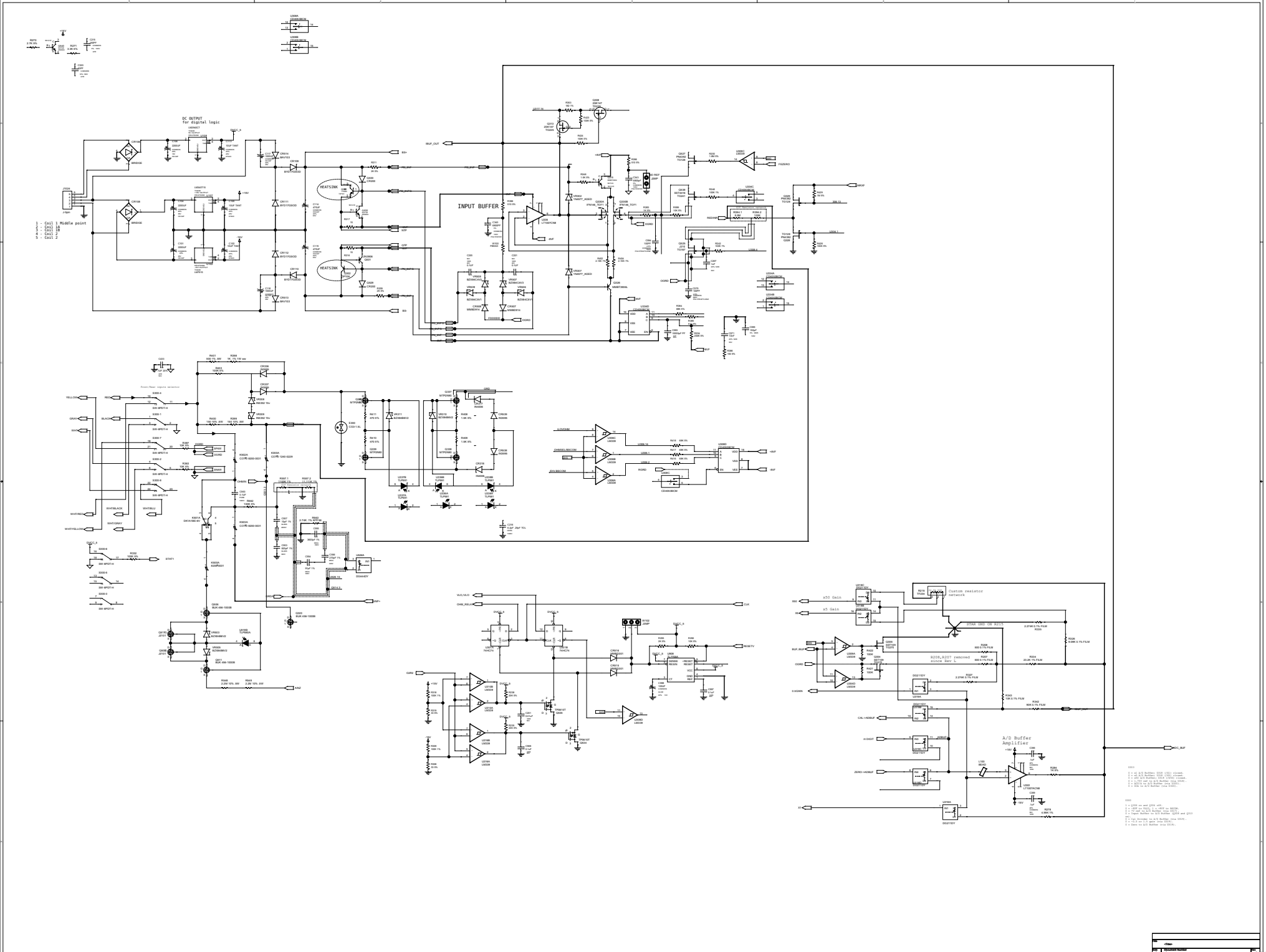
DC OUTPUT
for digital logic
INPUT BUFFER
HEATSINK
HEATSINK
1 - Coil 1 Middle point
2 - Coil 1A
3 - Coil 1B
4 - Coil 2
5 - Coil 2
Custom resistor network
x50 Gain
x5 Gain
A/D Buffer Amplifier
R208,R207 removed since Rev 1